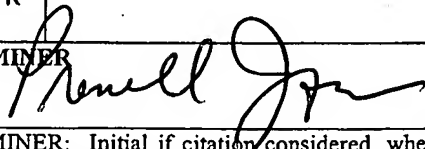


PTO-1449		Application No. 10/783,065		Applicant(s) Shimizu, et al.			
Information Disclosure Citation in an Application		Docket Number 073338.0177		Group Art Unit 2616		Filing Date February 20, 2004	
		U.S. PATENT DOCUMENTS					
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
	F						
	G						
NON-PATENT DOCUMENTS							
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
	H	Cyriel Minkenberg and Ton Engbersen, "A Combined Input and Output Queued Packet-Switched System Based on PRIZMA Switch-on-a-Chip Technology," IEEE Communications Magazine, pp. 70-77.					12/2000
	I	James P. G. Sterbenz and Joseph D. Touch, "High-Speed Networking," 5 pages.					2001
	J	Abhijit K. Choudhury and Ellen L. Hahne, "Dynamic Queue Length Thresholds for Shared-Memory Packet Switches," IEEE/ACM Transactions on Networking, , Vol. 6, No. 2, pp. 130-140.					4/1998
	K	M. Shreedhar and George Varghese, "Efficient Fair Queuing using Deficit Round Robin," pp. 1-22.					10/16/1995
	L						
	M						
	P						
	Q						
	R						
EXAMINER 				DATE CONSIDERED 12/31/07			
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